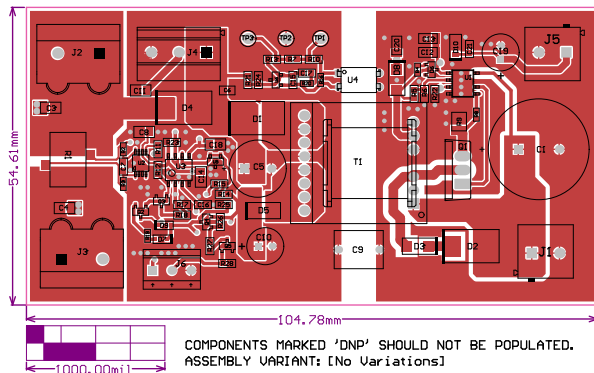


The PMP30191 Rev_C Reference Design has been built on PMP30191 Rev_A PCB.



DESIGN INFORMATION	
MIN. TRACK WIDTH:	8_MIL
MIN. CLEARANCE:	0.2 mm
MIN. VIA PAD SIZE:	24_MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- 5_MIL HOLES +/- 3_MIL HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3_MIL	
MATERIAL:	
<input type="checkbox"/> FR-408 <input checked="" type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER _____ THICKNESS: <input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10% <input type="checkbox"/> OTHER _____ TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____ BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____	
DRILLING:	
REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER _____	
BOARD FINISH:	
SILKSCREEN: <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER _____ SOLDER RESIST COLOR: <input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER _____ <input checked="" type="checkbox"/> MATTE <input type="checkbox"/> SEMI-GLOSS	
SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENG) <input type="checkbox"/> ENEPG <input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> OTHER _____	
ARRAY/PANEL: <input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE <input type="checkbox"/> N.C. ROUTE <input checked="" type="checkbox"/> V. SCORE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF: <input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3 <input type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER	
ALL BOARDS MUST MEET OR EXCEED UL94V-0 UL REGISTERED MATERIAL ID NUMBER PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS: MICROSECTION: <input type="checkbox"/> YES BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	



PROJECT TITLE:
120V-420Vdc 15W Isolated Flyback

DESIGNED FOR:
Public Release

FILE NAME:
PMP30191 Rev_A PCB.PcbDoc

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ENGINEER:
R. Scibilia

LAYOUT BY:
R. Scibilia

SCALE: 0.72

ALTIUM DESIGNER VERSION:
18.1.9.240

BOARD #: PMP30191	REV: A	SUN REV: Not In VersionControl
TID #:		
GENERATED : 4/11/2019 4:26:11 PM	TEXAS INSTRUMENTS	

PLOT NAME = Top Layer

1 2 3 4 5 6

A

A

B

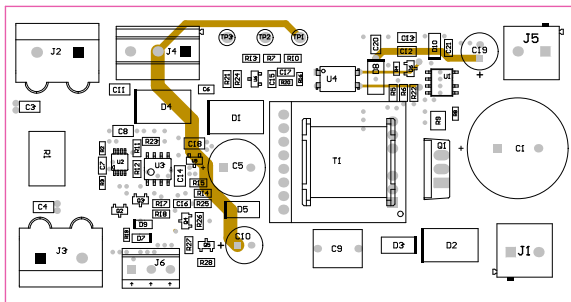
B

C

C

D

D



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
 ASSEMBLY VARIANT: [No Variations]

PROJECT TITLE: 120V-420Vdc 15W Isolated Flyback	
DESIGNED FOR: Public Release	
FILE NAME: PMP30191_Rev_A PCB.PcbDoc	
ENGINEER: R. Scibilia	LAYOUT BY: R. Scibilia
SCALE: 0.72	ALTIUM DESIGNER VERSION: 18.1.9.240

BOARD #: PMP30191	REV: A	SUN REV: Not In VersionControl
TID #:		
PLOT NAME = Inner Layer 1	GENERATED : 4/11/2019 4:26:11 PM	TEXAS INSTRUMENTS

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1 2 3 4 5 6

1 2 3 4 5 6

A

A

B

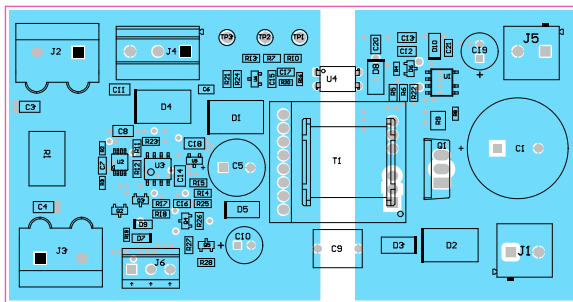
B

C


C

D

D



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: [No Variations]

	
PROJECT TITLE: 120V-420Vdc 15W Isolated Flyback	
DESIGNED FOR: Public Release	
FILE NAME: PMP30191_Rev_A PCB.PcbDoc	
ENGINEER: R. Scibilia	LAYOUT BY: R. Scibilia
SCALE: 0.72	ALTIUM DESIGNER VERSION: 18.1.9.240

BOARD #: PMP30191	REU: A	SUN REU: Not In VersionControl
TID #:		
GENERATED : 4/11/2019 4:26:11 PM	TEXAS INSTRUMENTS	

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1 2 3 4 5 6

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